

Industry Collaborative Workshop on “VLSI Circuit Design using VHDL”
Organised by The Department of ECE
in association with IIC SurTech, IETE-ISF and Livid Design Inc.

Date and Venue: 24th – 25th April, 2025, DSCSITSC

No of Students Participated: 43

Speaker/Coordinator: Dr Arindam Sadhu and Dr Pradipta Maiti

Department of ECE in association with IIC SurTech, IETE-ISF and Livid Design Inc. organised a one day workshop on IOT on 24th – 25th April, 2025. The preliminary objective was to involve the students of diploma colleges with modern technologies like design and fabrication and to inspire them for VHDL coding practice and to aware them about the current industry requirement for VLSI.

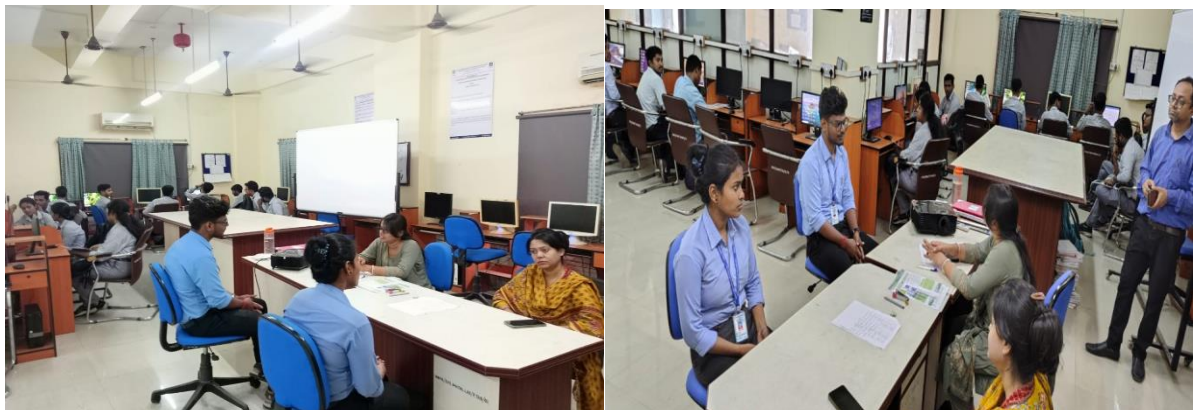
Professional trainers from Livid Design Inc. was present during the workshop. Students from WPC chandannagr, BIT Dunlop, TPI Chandannagar, BBIT Budgebudge were participated in the workshop

Outcome of the Workshop:

Students acquired the knowledge on the following domain of IOT,

1. Introduction to Chip Design
2. VHDL Simulation Environment
3. Circuit Analysis
4. Coding Protocols
5. Design Execution

Few glimpses of the event,





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Industry Collaborative Workshop on “VLSI Circuit Design using VHDL”

Make Ready Yourself for VLSI Industry as Front End Designer.....

REGISTRATION **FREE** for **Diploma
ETCE, EE and CSE** 3rd year students

Venue : **Dr. Sudhir Chandra Sur Institute
of Technology and Sports Complex**
(540, Dumdum Road, Surer Math)

Date : **24th and 25th April 2025**

Organized by : **Department of ECE, In
Association with IIC, SURTECH & IETE-ISF,
SURTECH & Livid Design INC.**
(Chip Design Start Up)

Topics :

- 1) Basics of Logic Circuits applicable for Chip Design.
- 2) Digital Logic Synthesis for Component Design in Processor Architecture.
- 3) CMOS Memory design.
- 4) Hands-on Training on “Xilinx” with Hardware Description Language (VHDL).

